

ECE 7711: Hardware DSP

Hardware implementation of real-time DSP. Parallel processing and architecture size/speed tradeoffs; Field Programmable Gate Arrays (FPGAs) and the use of the VHDL design language; development tools and libraries; FPGA implementation of filters and filter banks multirate processors, digital communication system components, implementation of advanced signal processing functions

Credits: 3.0

Prerequisites:

ECE 5790 :Y